Computer Hardware Description Languages and their Applications: Proceedings of the IFIP WG 10.2 Tenth International Symposium on Computer Hardware Description ... Hardware Description Languages and Their)

Dominique Borrione

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The topic areas presented within this volume focus on design environments and the applications of hardware description and modelling – including simulation, verification by correctness proofs, synthesis and test. The strong relationship between the topics of CHDL'91 and the work around the use and re-standardization of the VHDL language is also explored. The quality of this proceedings, and its significance to the academic and professional worlds is assured by the excellent technical programme here compiled.

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